



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Levine et al.

Group Art Unit: 2192

Serial No.: 10/674,642

Examiner: Das, Chameli

Filed: September 30, 2003

Attorney Docket No.: AUS920030484US1

For: Method and Apparatus to  
Autonomically Count Instruction  
Execution for Applications

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Respectfully submitted,

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

**Levine et al.**

Serial No: **10/674,642**

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Title: **Method and Apparatus to  
Autonomically Count Instruction Execution  
for Applications**

Group Art Unit: **2192**

Intellectual Property  
Law Department  
International Business  
Machines Corporation  
11400 Burnet Road  
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**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
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Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

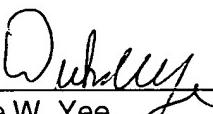
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Respectfully submitted,

  
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## FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS				
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

## NON-PATENT LITERATURE

<b>Examiner Initial</b>	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
AC	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

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